

## CLAIMS

1. An optical transmission system comprising:

an FEC generation section which generates and outputs  
an FEC frame; and an error correction section which performs  
5 error correction based on the FEC frame,

wherein the FEC generation section includes,

a first reading unit which sequentially stores  
first parallel data generated by demultiplexing information  
data and, thereafter, changes the order of the storage data  
10 on the basis of a predetermined standard to read the data;

a first encoding unit which performs first error  
correction encoding to the read data to generate first error  
correction codes;

a second reading unit which sequentially stores  
15 the first error correction codes and, thereafter, changes  
the order of the stored data into the original data to read  
the data;

a second encoding unit which performs second  
error correction encoding to the read data to generate second  
20 error correction codes; and

a frame generation unit which multiplexes the  
second error correction codes to generate the FEC frame,  
and

wherein the error correction section includes,

25 a first error correction unit which performs

error correction by a first decoding process to second parallel data generated by demultiplexing the FEC frame;

a third reading unit which sequentially stores the parallel data subjected to the first decoding process and, thereafter, changes the order of the stored data on the basis of a predetermined standard to read the data;

a second error correction unit which performs error correction by a second decoding process to the read parallel data;

a fourth reading unit which sequentially stores the parallel data subjected to the second decoding process and, thereafter, changes the order of the stored data into the original order to read the data; and

an information data reproduction unit which multiplexes the read parallel data to reproduce the original information data.

2. The optical transmission system according to claim 1, wherein combinations each constituted by the first error correction unit, the third reading unit, the second error correction unit, and the fourth reading unit are connected to each other in a plurality of stages.

3. An FEC multiplexing device provided on a transmission side of an optical transmission system, the FEC multiplexing

device comprising:

a first reading unit which sequentially stores parallel data generated by demultiplexing information data and, thereafter, changes the order of the stored data on the basis of a predetermined standard to read the data;

a first encoding unit which performs first error correction encoding to the read data to generate first error correction codes;

a second reading unit which sequentially stores the first error correction codes and, thereafter, changes the order of the stored data into the original data to read the data;

a second encoding unit which performs second error correction encoding to the read data to generate second error correction codes; and

a frame generation unit which multiplexes the second error correction codes to generate an FEC frame.

4. An FEC demultiplexing device provided on a reception side of a transmission system, the FEC demultiplexing device comprising:

a first error correction unit which performs error correction by a first decoding process to parallel data generated by demultiplexing an FEC frame;

a first reading unit which sequentially stores the

parallel data subjected to the first decoding process and, thereafter, changes the order of the stored data on the basis of a predetermined standard to read the data;

5 a second error correction unit which performs error correction by a second decoding process to the read parallel data;

10 a second reading unit which sequentially stores the parallel data subjected to the second decoding process and, thereafter, changes the order of the stored data into the original order to read the data; and

an information data reproduction unit which multiplexes the read parallel data to reproduce the original information data.

15 5. The FEC demultiplexing device according to claim 4, wherein combinations each constituted by the first error correction unit, the first reading unit, the second error correction unit, and the second reading unit are connected to each other in a plurality of stages.

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6. An error correction method comprising:

the FEC generation step of generating and outputting an FEC frame; and the error correction step of performing error correction based on the FEC frame,

25 wherein the FEC generation step includes,

the first parallel data generation step of demultiplexing information data, adding an OH (OverHead) data region and first and second redundant data regions to the demultiplexed information data, and inserting  
5 predetermined OH information into the OH data region to generate first parallel data;

the first reading step of sequentially storing the first parallel data and, thereafter, changes the order of the storage data on the basis of a predetermined standard  
10 to read the data;

the first encoding step of performing first error correction encoding to the read data and storing the redundant information of the data in the first redundant data region to generate to generate first error correction  
15 codes;

the second reading step of sequentially storing the first error correction codes and, thereafter, changes the order of the stored data into the original data to read the data;

20 the second encoding step of performing second error correction encoding to the read data and storing the redundant information of the data in the second redundant data region to generate second error correction codes; and

the frame generation step of multiplexing the  
25 second error correction codes to generate an FEC frame, and

wherein the error correction step includes,

the second parallel data generation step of demultiplexing the FEC frame and establishing frame synchronism after the demultiplexing to generate second  
5 parallel data;

the first error correction step of performing error correction by a first decoding process to the second parallel data;

the third reading step of sequentially storing  
10 the parallel data subjected to the error correction and, thereafter, changes the order of the stored data on the basis of a predetermined standard to read the data;

the second error correction step of performing error correction by a second decoding process to the read  
15 parallel data; and

the fourth reading step of sequentially storing the parallel data subjected to the error correction and, thereafter, changes the order of the stored data into the original order to read the data; and

20 the information data reproduction step of separating the overhead information from the read parallel data, deleting the OH data regions and the redundant data regions, and multiplexing the parallel data to reproduce the original information data.

5 the fourth reading step is performed plural times.